

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#### PATENT APPLICATION

Applicant : Carl T. Nelson

Application No.: 08/794,374 Confirmation No.: 6586

Filed : December 10, 1996

For : SWITCHING VOLTAGE REGULATOR CIRCUIT

Group Art Unit : 2838

Examiner : Shawn Riley

Palo Alto, California 94301 May 13, 2005

Mail Stop: Issue Fee

Hon. Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

### SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

During a review of applicant's file for the present reissue application, applicant's representatives noticed that, with an Office action dated May 16, 1995, Examiner Riley returned a Form PTO-1449 (hereinafter "the 3.9.95 Form PTO-1449") that applicant had submitted to the USPTO on March 9, 1995 with a Supplemental Information Disclosure Statement and that Examiner Riley had considered on May 12, 1995. On the 3.9.95 Form PTO-1449, Examiner Riley indicated that three references were "[n]ot found." Presumably, those three references were not found among the copies of references applicant had submitted to the USPTO

with the 3.9.95 Form PTO-1449 and the corresponding Supplemental Information Disclosure Statement.

Accordingly, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicant resubmits hereinbelow the three references that Examiner Riley indicated on the 3.9.95 Form PTO-1449 to not have been found. \*

## Other Documents

Data Sheet, "LM117/LM217/LM317 3-Terminal Adjustable Regulator," National Semiconductor Corporation Linear Databook, 1982, pp. 1-23 to 1-30.

Application Note AN-110, "Fast IC Power Transistor with Thermal Protection," National Semiconductor Corporation, May 1974, pp. AN110-1 to AN110-6.

Data Sheet, "LT1005M/LT1005C Logic Controlled Regulator," Linear Technology Corporation, Linear Databook, 1985, pp. 259-270.

All of the above documents also are listed on the accompanying Form PTO/SB/08a. Copies of the aforementioned documents are enclosed herewith.

Applicant's submission of this statement is not an admission that the references herein are, or are considered to be, material to patentability of any presented claim. With respect to cited documents other than patents, applicant has identified dates or possible date codes that appear on the documents. Applicant's identification of these dates is not an admission that the documents were published by or on the dates identified. Applicant reserves the right to challenge the status of any of the cited documents and information as prior art.

Respectfully submitted,

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December 10, 1996

Carl T. Nelson

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Filing Date

Art Unit

First Named Inventor

# TRANCE INFORMATION DISCLOSURE STATEMENT BY APPLICANT

2838 **Examiner Name** Shawn Riley (Use as many sheets as necessary) Attorney Docket Number 1 LT-5 RE RCE Sheet of 11

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number Number-Kind Code <sup>2 (if known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

		FOREIGN P	ATENT DOCU	MENTS		
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		Data Sheet, "LM117/LM217/LM317 3-Terminal Adjustable	
		Regulator," National Semiconductor Corporation Linear	
		Databook, 1982, pp. 1-23 to 1-30.	
		Application Note AN-110, "Fast IC Power Transistor with	
		Thermal Protection," National Semiconductor	
		Corporation, May 1974, pp. AN110-1 to AN110-6.	
		Data Sheet, "LT1005M/LT1005C Logic Controlled	
		Regulator," Linear Technology Corporation, Linear	
		Databook, 1985, pp. 259-270.	

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Examiner		Date	
		56.0	
Signature		Considered _l _	

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.